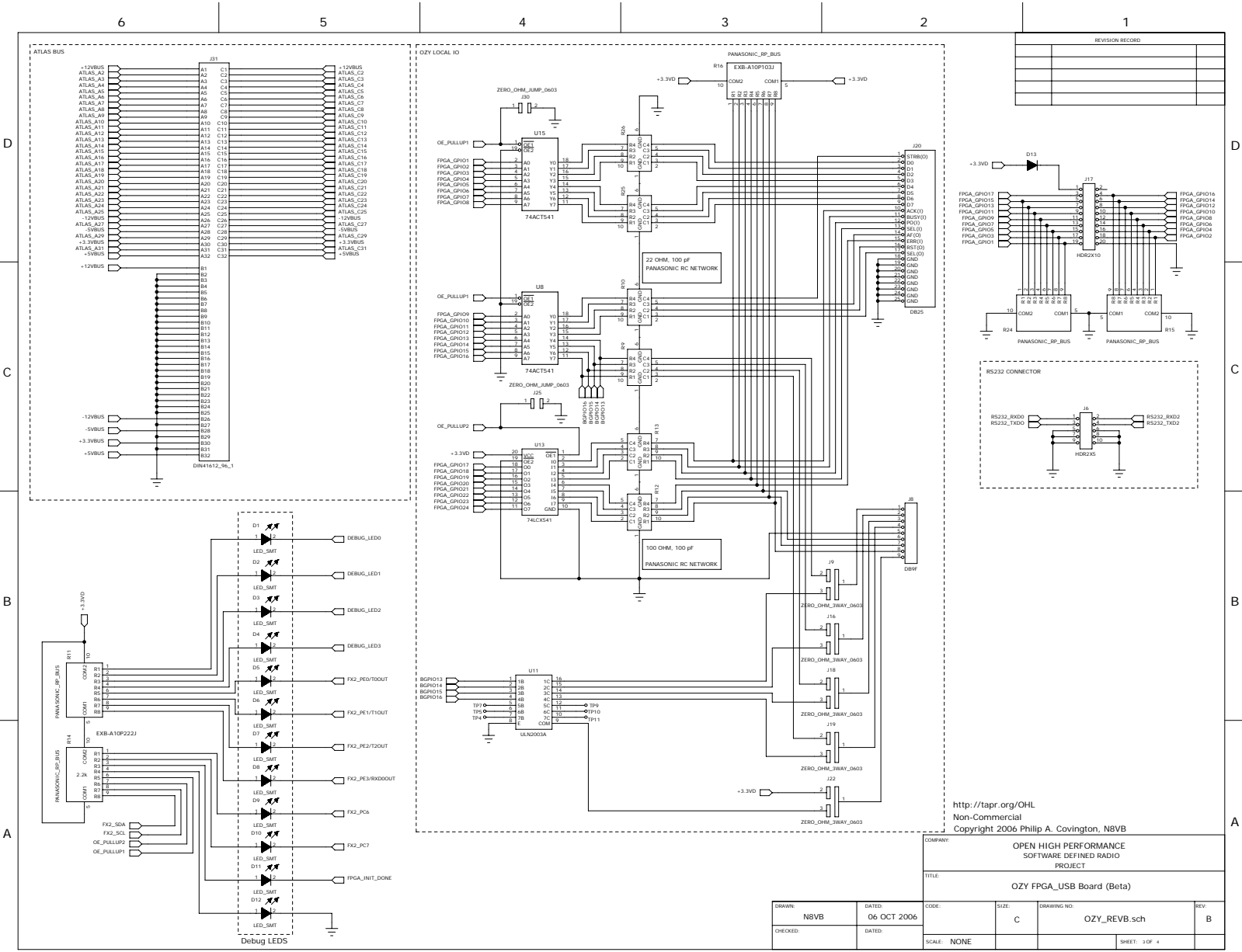


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COMPANY: OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT

TITLE: OZY FPGA_USB Board (Beta)

DRAWN: N8VB	DATED: 06 OCT 2006	CODE:	SIZE: C	DRAWING NO: OZY_REV3.sch	REV: B
CHECKED:	DATED:	SCALE: NONE	SHEET: 2 OF 4		



REVISION RECORD	

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TITLE:		OZY FPGA_USB Board (Beta)			
DRAWN:	N8VB	DATED:	06 OCT 2006	CODE:	J19
CHECKED:		DATED:		SIZE:	C
SCALE:		NONE		DRAWING NO.:	OZY_REV.B.sch
				REV.:	B
				SHEET: 3 OF 4	

6

5

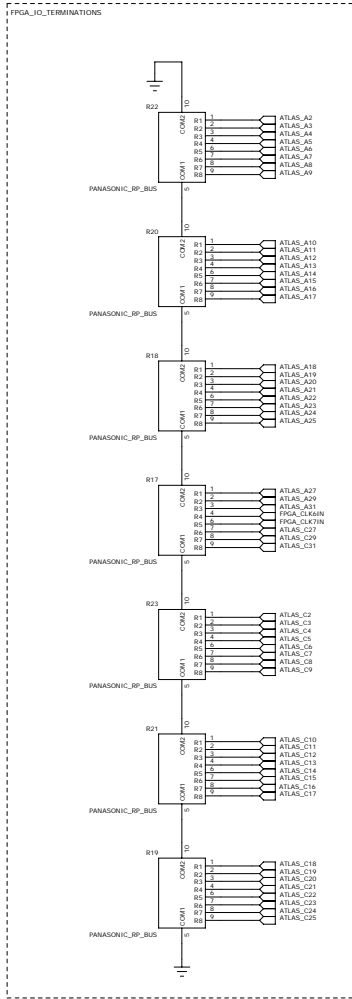
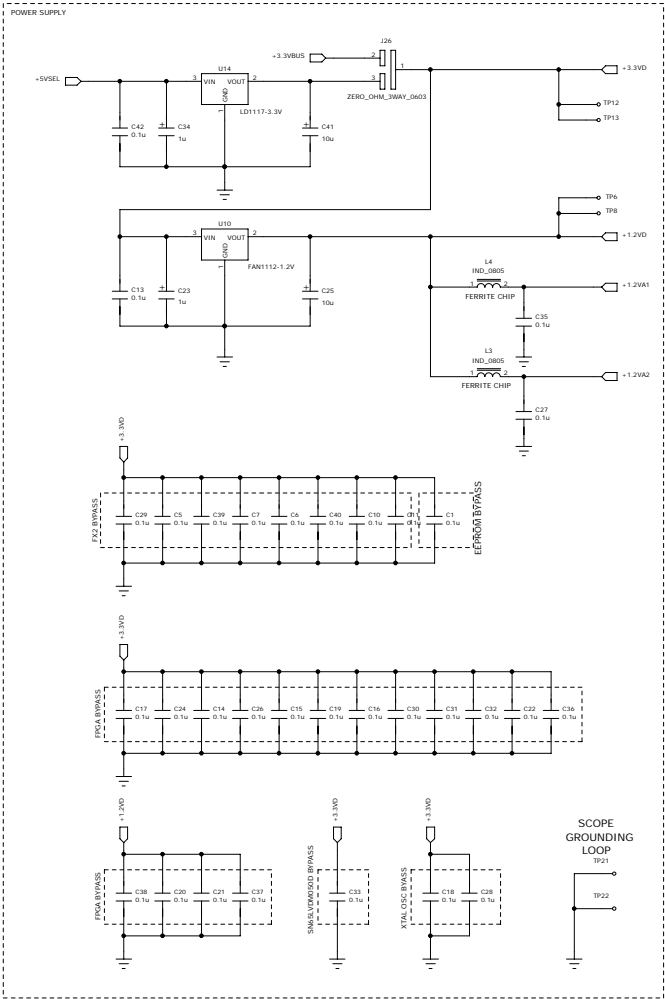
4

3

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1

REVISION RECORD	



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DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO.:	REV:
N8VB	06 OCT 2006		C	OZY_REV.B.sch	B
CHECKED:	DATED:	SCALE: NONE		SHEET: 4 OF 4	